<u>Listing of Claims</u>

The claims pending in the subject-application as of this Amendment are as follows:

1.   (Currently Amended) In a device that alerts a user to an incoming message by activating <u>one of</u> an acoustic driver <u>and a vibrator</u>, a method for <u>automatically placing the device in a quiet mode of operation</u> [shunting the acoustic driver] comprising the steps of:

        a)   detecting the presence of a broadcast squelch signal by monitoring signals that arrive at the device from an emitter; ~~and~~

        b)   <u>generating a control signal in response to the detection of the broadcast squelch signal</u> ~~automatically shunting the acoustic driver in response to the detecting step free of any communication back to the emitter~~;

        c)   <u>writing to a buffer memory a first binary value which signifies the quiet mode of operation in response to the control signal; and</u>

        d)   <u>controlling a state of an alert mode switch based on the contents of the buffer memory, so as to energize the vibrator in response to the incoming message when the binary value in the buffer memory is the first binary value.</u>

2.   (Original) The method as in claim 1, wherein the squelch signal originates extrinsic to the device.

Claims 3 - 4: Canceled.

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5. (Original) The method as in claim 1, wherein the detecting step comprises comparing incoming signals to an expected signal pattern to detect the presence of a squelch signal.

6. (Original) The method as in claim 1, wherein the detecting step comprises processing incoming signals to extract, when present, an indicium of the presence of the squelch signal and thereby detect the presence of a squelch signal.

7. (Original) The method as in claim 1, including the additional step of shunting the acoustic driver for a period of time after the broadcast squelch signal is detected.

8. (Original) The method as in claim 7, wherein the step of shunting the acoustic driver continues for a period of time after the broadcast squelch signal is no longer present.

Claims 9 - 12: Canceled.

13. (Currently Amended) An electronic device of the type which alerts a user to an incoming message by connecting an alert signal to a preselected one of first and second alert devices, comprising:

a) a detector which monitors the incoming message to detect the presence of a squelch signal broadcast locally by an emitter and generates a control signal at its output when the squelch signal is detected;

b) a processor operatively connected to the output of the detector;

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c)   an alert-mode memory-cell storing one of a default binary value and a user-set binary value;

d)   a buffer memory connected to the processor and configured to store a predetermined one of two binary values therein in response to the control signal when the squelch signal is detected and also configured to store the contents of the alert-mode memory-cell in the absence of detection of the squelch signal;

e)   a switch, operatively connected to the ~~output of the detector~~processor, to automatically direct the alert signal to a predetermined one of the first and second alert devices while the squelch signal is being detected as a function of the binary value stored in the buffer memory~~,~~

~~wherein the electronic device operates free of any communications back to the emitter~~.

Claims 14 - 16: Canceled.

17.   (Newly Presented) The method of claim 1, including the additional step of writing a second binary value which is different than the first binary value to the buffer memory, wherein the state of the alert mode switch is set so as to energize the acoustic driver in response to the incoming message.

18.   (Newly Presented) The method of claim 17, wherein the second binary value is stored in a user-set alert mode memory.

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19.   (Newly Presented) The method of claim 17, wherein the additional step of writing the second binary value is performed when the broadcast squelch signal ceases being detected.

20.   (Newly Presented) The method of claim 1, wherein the detecting step monitors a header of the incoming message for inclusion of the broadcast squelch signal.

21.   (Newly Presented) The method of claim 1, wherein the buffer memory is configured to store only one bit.

22.   (Newly Presented) The electronic device of claim 13, further comprising a circuit configured to populate the buffer memory with the contents of the alert-mode memory-cell when the broadcast squelch signal ceases being detected.

23.   (Newly Presented) The electronic device of claim 13, further comprising a software program which executes in the processor so as to populate the buffer memory with the contents of the alert-mode memory-cell when the broadcast squelch signal ceases being detected.

24.   (Newly Presented) The method of claim 13, wherein the buffer memory is configured to store only one bit.

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